



PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

April 5, 2004

Date

Denise Sheridan

Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	:	Brian W. Huber and David R. Brown	Attorney Docket No.:	500125.02
Serial No.	:	10/007,871	Group Art Unit	: 2818
Filed	:	November 13, 2001	Confirmation No.	: 8896
Title : METHOD OF SCALING DIGITAL CIRCUITS AND CONTROLLING THE TIMING RELATIONSHIP BETWEEN DIGITAL CIRCUITS				
Date of Notice of Allowance : January 6, 2004				

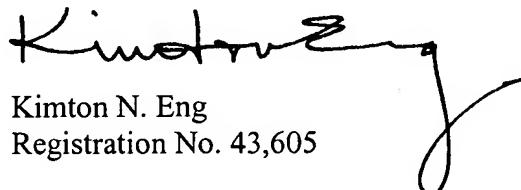
FILING FORMAL DRAWINGS AFTER ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed are 4 sheets of formal drawings, Figures 1-10 for filing in the above-identified application.

Respectfully submitted,
DORSEY & WHITNEY LLP


Kimton N. Eng
Registration No. 43,605

KNE:dms

Enclosures:

Postcard
Formal Drawings (Figures 1-10)
Copy of Notice of Allowance

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